JUL 2 7 2001		
E JOE LE	Docket Number (Optional)	Application Number
MEORMATION DISCLOSURE STATEMENT	SAA-55 (401 P 233)	09/903,899
INFORMATION DISCLOSURE STATEMENT	Applicant(s)	
	Webster et al.	
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	Filing Date	Group Art Unit
⊸ Y	7/12/01	Not Yet Assigned

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	NAME	FILING DATE (IF APPROPRIATE)
(nd	6,073,053	6/6/00	Dummermuth	
	·			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION (YES/NO)

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

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Examiner:

Date Considered: 10/29/03

Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant. (127027.1)

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Attorney Docket No. SAA-55 (402 P 233)

Application No.: 09/903,899

Applicant: Webster et al. Filing Date: July 12, 2001

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Art Unit: 2181

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Technology Center 2100

U.S. PATENT DOCUMENTS

Examiner Initial	U.S. Patent Document No.	Name of Patentee or Applicant	Class/ Subclass	Date of Publication
(2)	6,151,625	Swales et al.	709/218	11/21/2000
	6,327,511 B1	Naismith et al.	700/19	12/04/2001

OTHER PRIOR ART OR NON-PATENT LITERATURE DOCUMENTS

Examiner Initial	(including Author (in capital letters), Title of the article, Title of the item, Date, Pages, Volume-Issue number, Publisher, City and/or Country where published.)	Т
	INTEL CORPORATION, "Back to Basics: Interrupts for the 8XC196KC/KD Using Standard ISRs," http://www.intel.com/design/MCS96/TECHNOTE/2672.HTM, January 29, 2002, pp. 1-7	
Ø	HOWE, D., "interrupt from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?query=interrupt&action=Search, January 31, 2002, p. 1	
(red	HOWE, D., "asynchronous from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?asynchronous, January 31, 2002, p. 1	
	HOWE, D., "control structure from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?control+structures, January 31, 2002, p. 1	
nov	HOWE, D., "interrupt handler from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?interrupt+handler, January 31, 2002, p. 1	
(1d)	HOWE, D., "hardware from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?hardware, January 31, 2002, p. 1	
	HOWE, D., "software from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?software, January 31, 2002, p. 1	
	HOWE, D., "system call from FOLDOC," http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?system+call, January 31, 2002, p. 1	

RECEIVED

Attorney Docket No. SAA-72 (402 P 275) Application No. 10/096,354

JUN 2 0 2002

Page 2

Technology Center 2100

TPE	Technology Center 210
JUN 1 7 2002	ROEHRL, G., "8086/8088 Interrupts and DOS System Programming," http://www.ece.wpi.edu/~wrm/Courses/EE3803/Labs/roehrl.html, January 31, 2002, pp. 1-13
TO TRADE	TECHTARGET, INC., "What IsIRQ (a definition), http://iroi.seu.cn/books/whatis/irq/htm, January 16, 2002, p. 1
(ye)	TECHTARGET, INC., "What Isan interrupt (a definition), http://iroi.seu.cn/books/whatis/interrup/htm, January 16, 2002, p. 1
The state of the s	ADDTRON TECHNOLOGY CO., LTD., "Interrupts," http://www.addtron.com/test1/interupt.htm, January 16, 2002, pp. 1-5
	AASHEIM, J., CORNELIUS, T., "Interrupt Programming," slide presentation, February 1, 2000, pp. 1-8
- A	RYAN, D. J., "Interrupts," http://zeus.cs.pacificu.edu/~ryand/cs320/ch5c99.htm, January 18, 2002, pp. 1-2
R	LEESON, I. J., "Interrupts, What Are They and What Do They Do?", http://cq-pan.cqu.edu.au/students/ivanl1/intwhdo.html, January 18, 2002, pp. 1-2
h	ST, "ST10X167/ST10F168: Programming ST10X167/ST10F168 CAN interrupt drivers," October 12, 1998, pp. 1-17.
be	HEIDENSTROM, K., "FAQ / Application notes: Timing on the PC family under DOS," http://www.contactor.se/~daniel/links/Pctim003.txt, January 29, 2002, pp. 1-138
Ø	SCHNEIDER AUTOMATION, INC., "Chapter 1: Ladder Logic Operating System for the Micro PLCs," Modicon Micro Controllers Ladder Logic Manual, 1997, pp. 1-16
fig.	SCHNEIDER ELECTRIC, "Real time control Momentum M1E Processor Adapter over Ethernet," March 2000, pp. 1-7
AP.	"Chapter 1: Theory of Operations," website unknown, date unknown, pp.1-
66	SCHNEIDER AUTOMATION, INC., "Chapter 10: Subroutine Instructions," Modicon Micro Controllers Ladder Logic Manual, 1997, pp. 1-16
15	MODICON, "512/612 Micro PLC: Hardware User Manual," pp. 1-60

Examiner L

Date

Considered: 10/29/03

Attorney Docket No. SAA-72 (402 P 275) Application No. 10/096,354 Page 3



RECEIVED

JUN 2 0 2002

Technology Center 2100

Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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